

Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
1	BRS L1	15	semiconductor AND substrate AND ((flat flatten\$3) WITH (suck sucking))	USPAT; USOCR; EPO; JPO; Derwent	2000/03/25 09:23			0
2	BRS L3	1	1 AND ((insulating dielectric) WITH (film layer) WITH substrate)	USPAT; USOCR; EPO; JPO; Derwent	2000/03/25 09:25			0

09/30/94, 523

EXOT suck f/t suck.wsp

→ on relevant pat, the patent post, w 5,907,770.

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
1	BRS	L6	22	((FLATTEN\$3 ADJ3 SUBSTRATE) SAME "LIQUID CRYSTAL")	USPAT; USOCR; EPO; JPO; Derwent	2000/03/25 10:38			0

09/30/97, 523

EAST search 2/10/7.wsp